

Connection between COOL-1300/Q and National Instruments PCI-1422, LVDS

COOL-1300/Q		COOL-1300/Q		PCI-1422		PCI-1422	
Pin	Funktion	Twisted pair	Pin	Funktion	Color		
1	PCLK	1	49	Pixel Clock +			
2	LEN	2	43	Enable B+			
3	FEN	3	41	Enable A+			
4	D0 (LSB)	4	1	DATA 0+			
5	D1	5	3	DATA 1+			
6	D2	6	5	DATA 2+			
7	D3	7	7	DATA 3+			
8	D4	8	9	DATA 4+			
9	D5	9	11	DATA 5+			
10	D6	10	13	DATA 6+			
11	D7	11	15	DATA 7+			
12	D8	12	17	DATA 8+			
13	D9	13	19	DATA 9+			
14	D10	14	21	DATA 10+			
15	D11 (MSB)	15	23	DATA 11+			
16	GND		99	GND			
control input 17	$\overline{\text{TREX}}$ (trigger input; activ low)		33	Control 0+			
control input 18	$\overline{\text{SV2}}$ (amplification * 2; activ low)		35	Control 1+			
control input 19	$\overline{\text{BIN}}$ (binning mode; activ low)		37	Control 2+			
20	$\overline{\text{PCLK}}$	1	50	Pixel Clock -			
21	$\overline{\text{LEN}}$	2	44	Enable B-			
22	$\overline{\text{FEN}}$	3	42	Enable A-			
23	$\overline{\text{D0}}$	4	2	DATA 0-			
24	$\overline{\text{D1}}$	5	4	DATA 1-			
25	$\overline{\text{D2}}$	6	6	DATA 2-			
26	$\overline{\text{D3}}$	7	8	DATA 3-			
27	$\overline{\text{D4}}$	8	10	DATA 4-			
28	$\overline{\text{D5}}$	9	12	DATA 5-			
29	$\overline{\text{D6}}$	10	14	DATA 6-			
30	$\overline{\text{D7}}$	11	16	DATA 7-			
31	$\overline{\text{D8}}$	12	18	DATA 8-			
32	$\overline{\text{D9}}$	13	20	DATA 9-			
33	$\overline{\text{D10}}$	14	22	DATA 10-			
34	$\overline{\text{D11}}$	15	24	DATA 11-			
35	GND		100	GROUND			
control input 36	TREX (trigger input)						
control input 37	LED<250K						